

30V N-Channel Enhancement Mode MOSFET

Description

The PECN4446SR uses trench MOSFET technology that is uniquely optimized to provide the most efficient high frequency switching performance. Conduction and switching losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Cr_{ss} .

General Features

- ◆ $V_{DS} = 30V$ $I_D = 15A$
 $R_{DS(ON)}(Typ.) = 6.8m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(ON)}(Typ.) = 9.0m\Omega$ @ $V_{GS} = 4.5V$
- ◆ Lead free product is acquired
- ◆ Surface mount package

Application

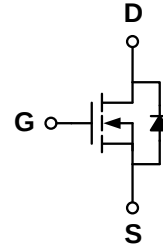
- ◆ High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- ◆ Networking DC-DC Power System
- ◆ Load switch

Package

- ◆ SOP-8

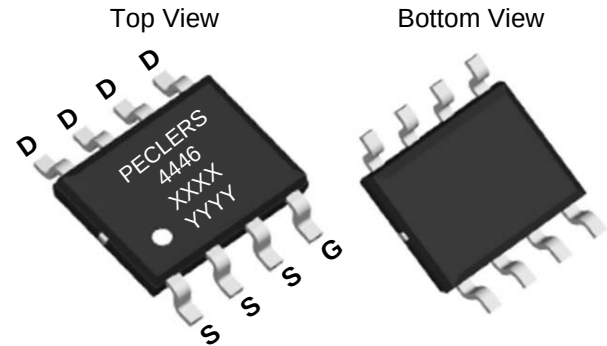
100% UIS TESTED!
100% ΔV_{ds} TESTED!

Schematic diagram



Marking and pin assignment

SOP-8



XXXX—Date Code
 YYYY—Quality Code.



Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
PECN4446SR	-55°C to +150°C	SOP-8	4000

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

parameter		symbol	limit	unit
Drain-source voltage		V_{DS}	30	V
Gate-source voltage		V_{GS}	±20	V
Continuous Drain Current	TC=25°C	I_D	15	A
	TC=70°C		12	
Pulsed Drain Current		I_{DP}	40	A
Avalanche energy(L=0.1mH)		E_{AS}	50	mJ
Power Dissipation	TC=25°C	P_D	3	W
	TC=70°C		2.1	
Operating junction Temperature range		T_j	-55—150	°C

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS}=30V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ C$	-	-	5	
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.8	1.6	2.2	V
Drain-source on-state resistance ¹	$R_{DS(ON)}$	$V_{GS}=10V, I_D=15A$	-	6.8	8.5	m Ω
		$V_{GS}=4.5V, I_D=11A$		9	12	
On Status Drain Current	$I_{D(ON)}$	$V_{DS}=10V, V_{GS}=5V$	40	-	-	A
Diode Characteristics						
Diode Forward Voltage	V_{SD}	$I_{SD}=1A, V_{GS}=0V$	-	0.82	1.1	V
Diode Continuous Forward Current	I_S		-	-	4	A
Reverse Recovery Time	t_{rr}	$I_F=15A,$	-	28	-	ns
Reverse Recovery Charge	Q_{rr}	$dI/dt=100A/\mu s$	-	40	-	nC
Dynamic Characteristics						
Gate Resistance	R_G	$V_{GS}=0V, V_{DS}=0V, f=1MHz$	-	0.47	0.7	Ω
Input capacitance	C_{ISS}	$V_{GS}=0V, V_{DS}=15V$ $f=1.0MHz$	-	985	-	pF
Output capacitance	C_{OSS}		-	205	-	
Reverse transfer capacitance	C_{RSS}		-	76	-	
Turn-on delay time	$t_{D(ON)}$	$V_{GS}=10V, V_{DS}=15V, R_L=1\Omega,$ $R_G=3\Omega$	-	7	-	ns
Turn-on Rise time	t_r		-	8	-	
Turn-off delay time	$t_{D(OFF)}$		-	22	-	
Turn-off Fall time	t_f		-	6.8	-	
Total gate charge	Q_g	$V_{GS}=10V, V_{DS}=15V, I_D=15A$	-	18	27	nC
Gate-source charge	Q_{gs}			2.4		
Gate-drain charge	Q_{gd}		-	2.3	-	

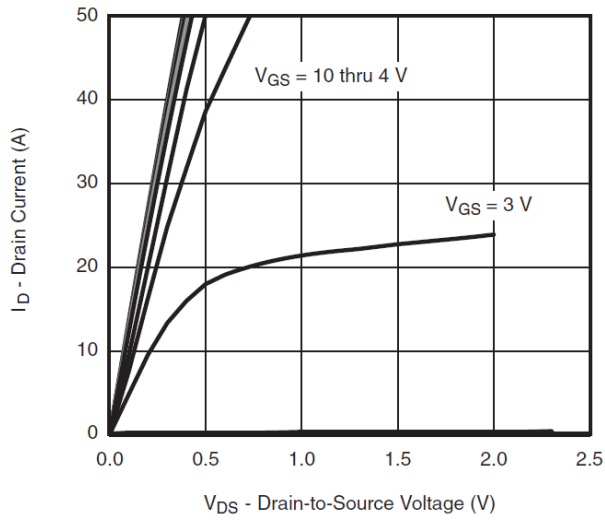
Thermal Characteristics

Parameter		Symbol	Typ	Max	Unit
Maximum Junction-to-Ambient ^A	$\leq 10s$	$R_{\theta JA}$	33	40	$^\circ C/W$
Maximum Junction-to-Ambient ^A	Steady-State		59	75	
Maximum Junction-to-Lead ^B	Steady-State	$R_{\theta JC}$	16	24	

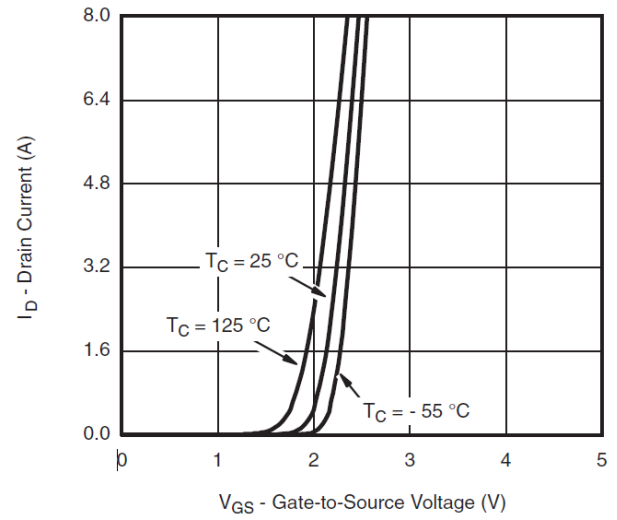
A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ C$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10s$ thermal resistance rating.

B: The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

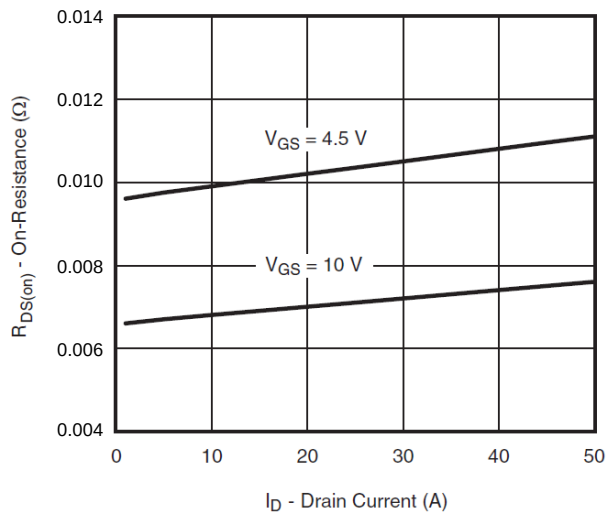
Typical Performance Characteristics



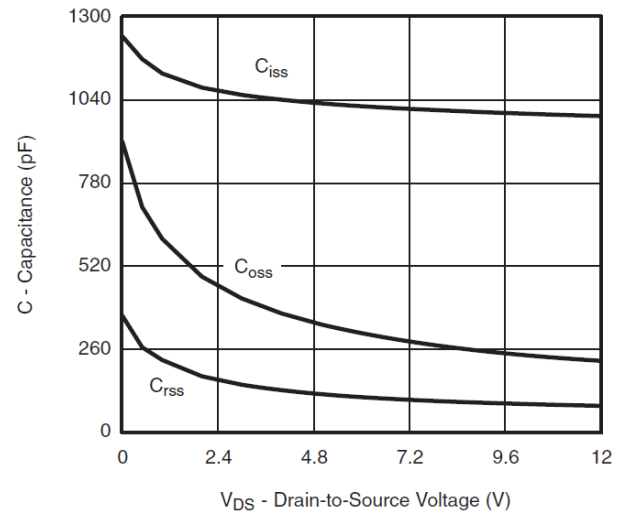
Output Characteristics



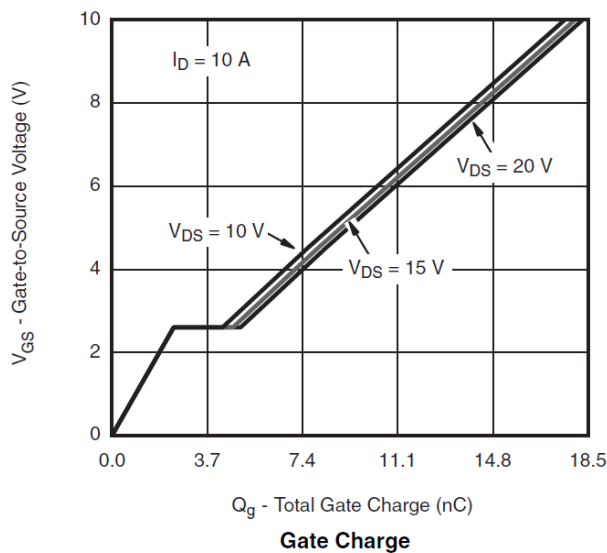
Transfer Characteristics



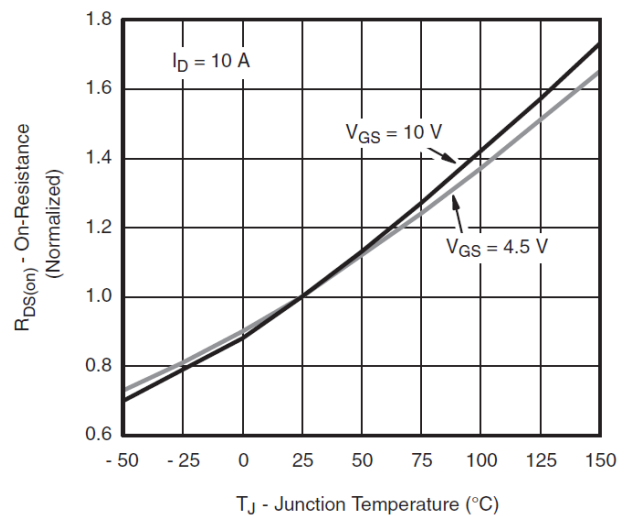
On-Resistance vs. Drain Current and Gate Voltage



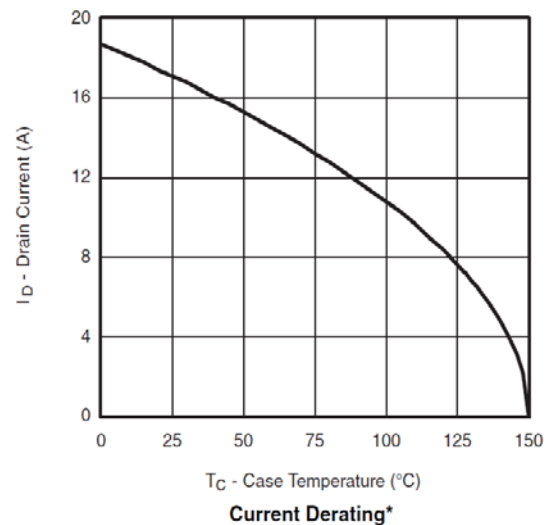
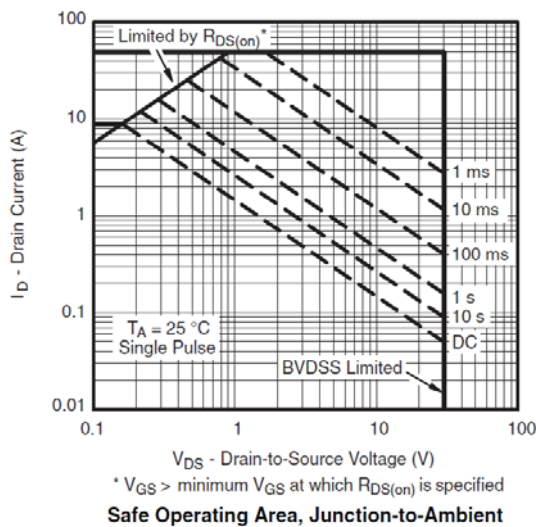
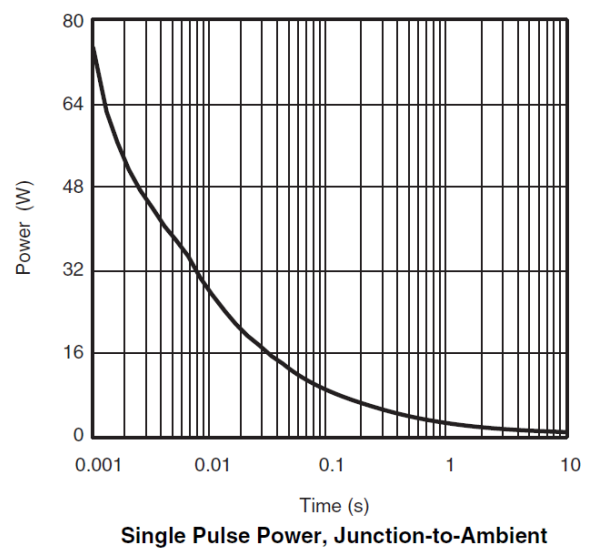
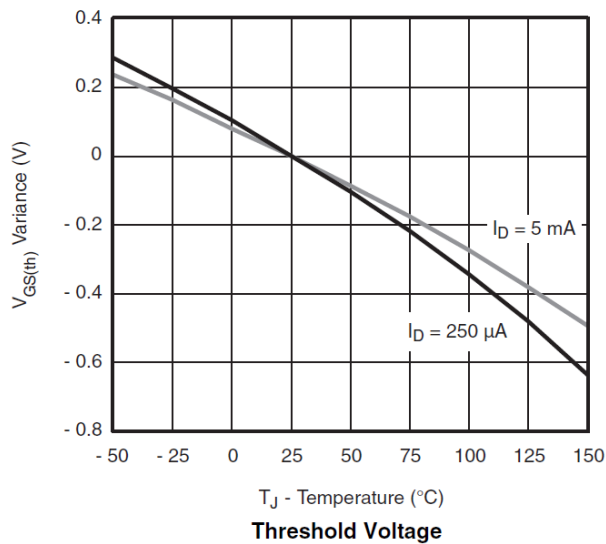
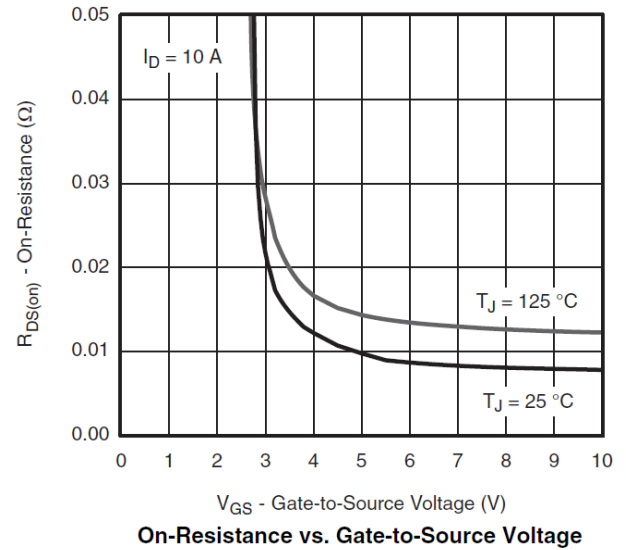
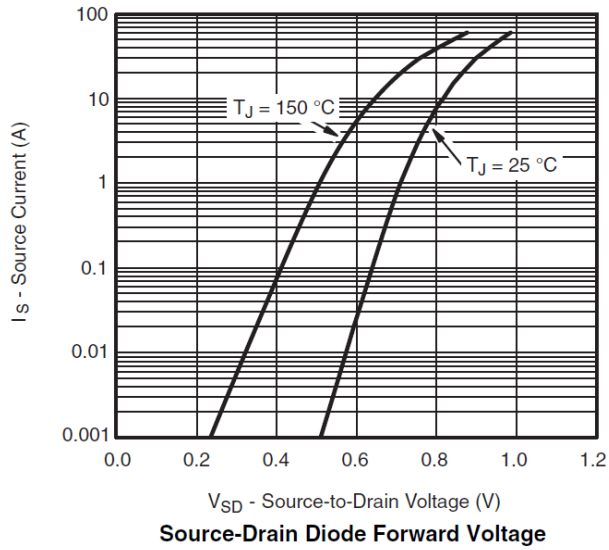
Capacitance

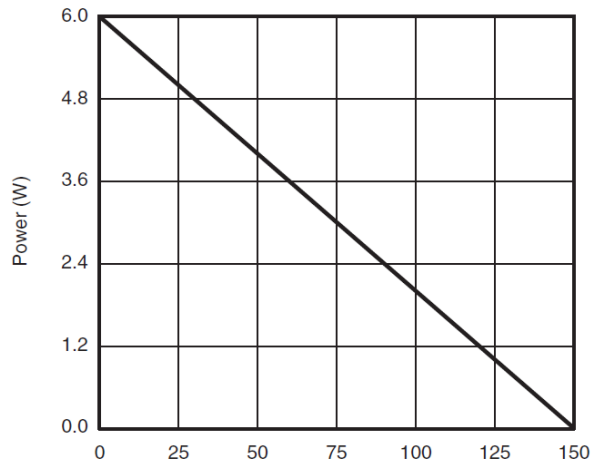


Gate Charge

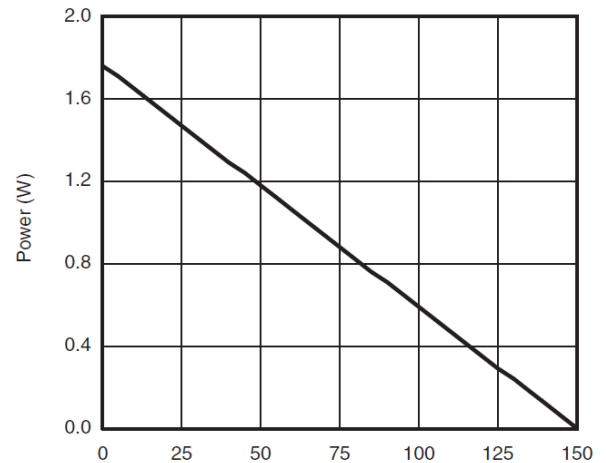


On-Resistance vs. Junction Temperature

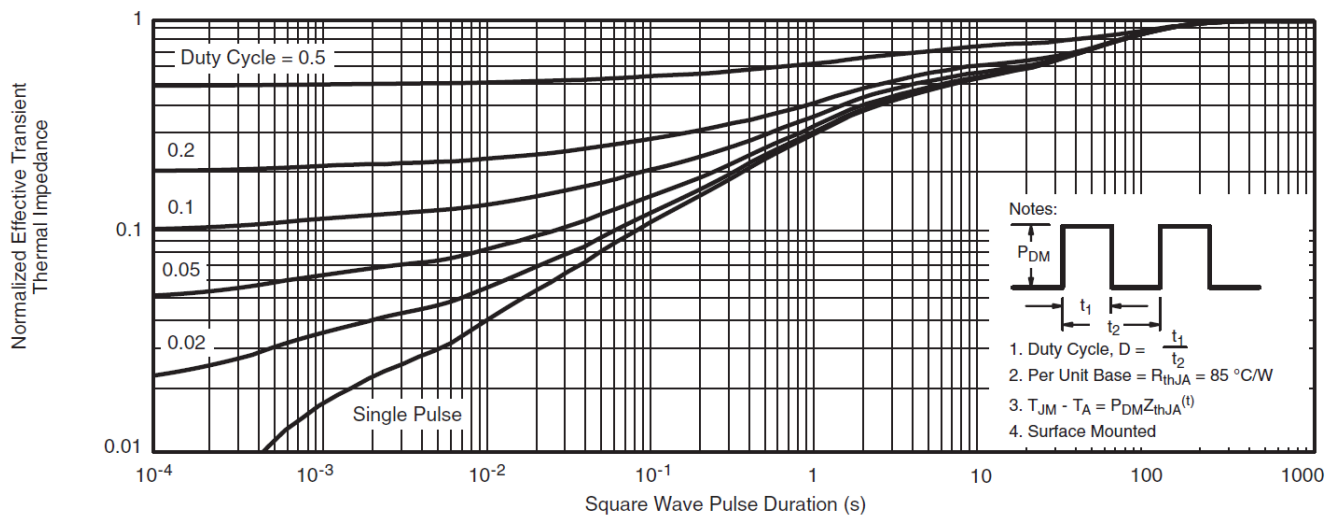




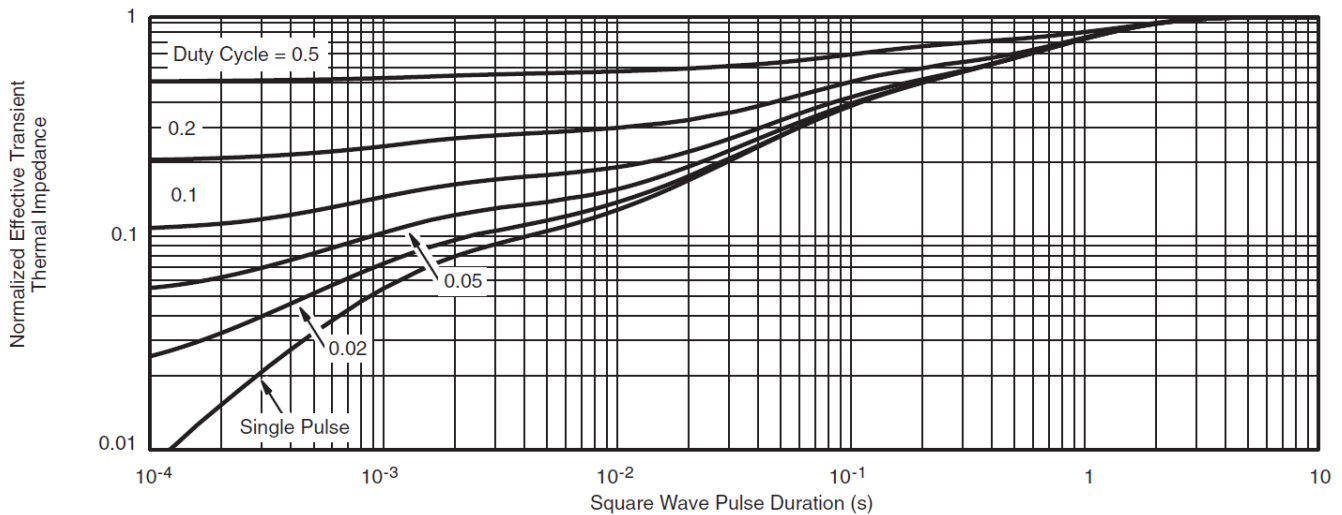
T_C - Case Temperature (°C)
Power, Junction-to-Ambient



T_A - Ambient Temperature (°C)
Power Derating, Junction-to-Foot



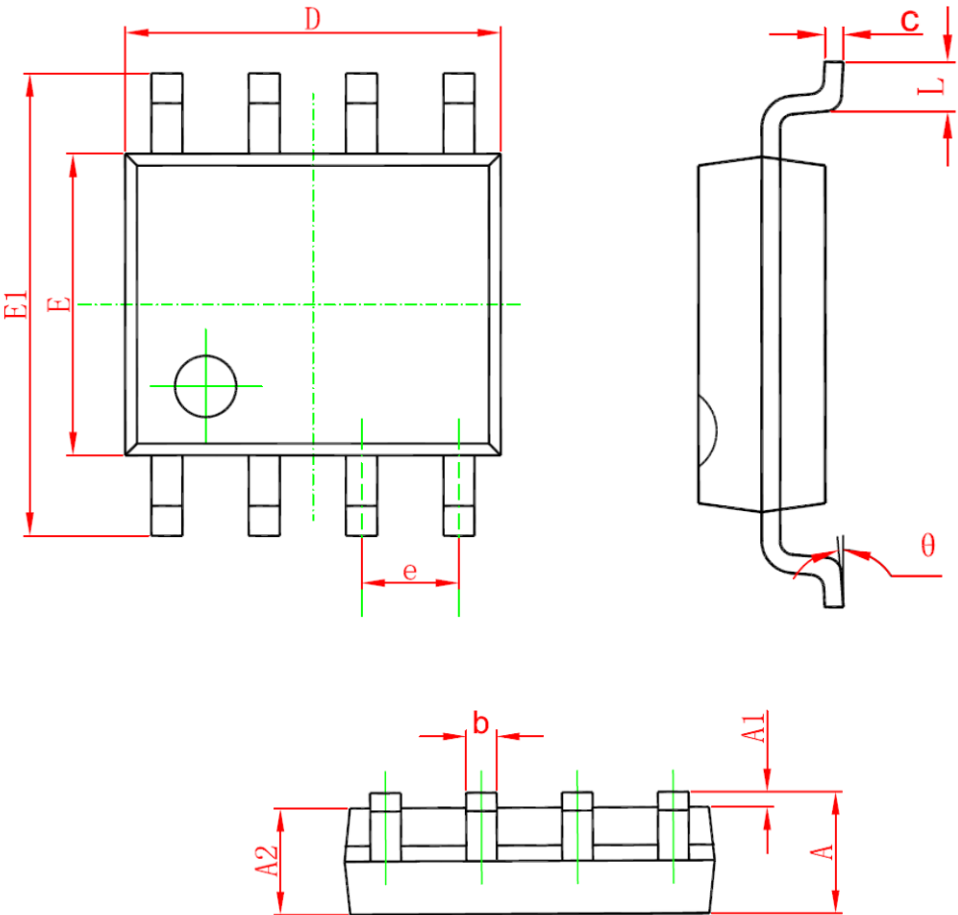
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Package Information

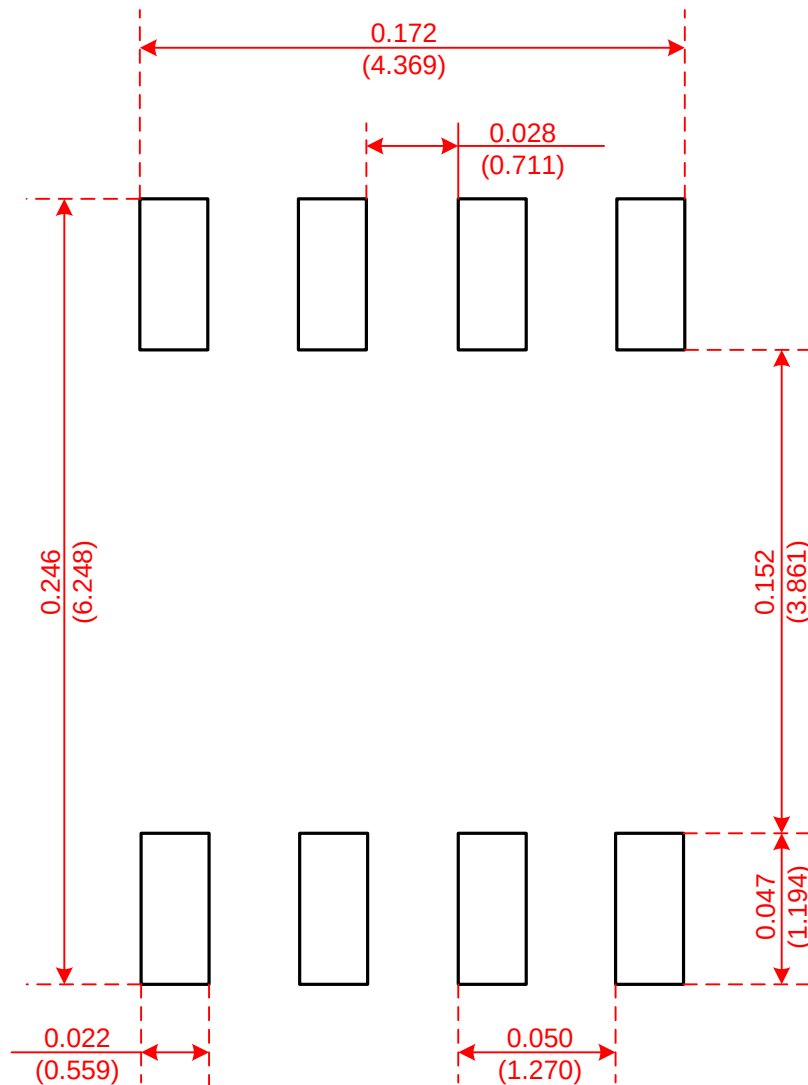
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Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

Recommended Minimum Pads

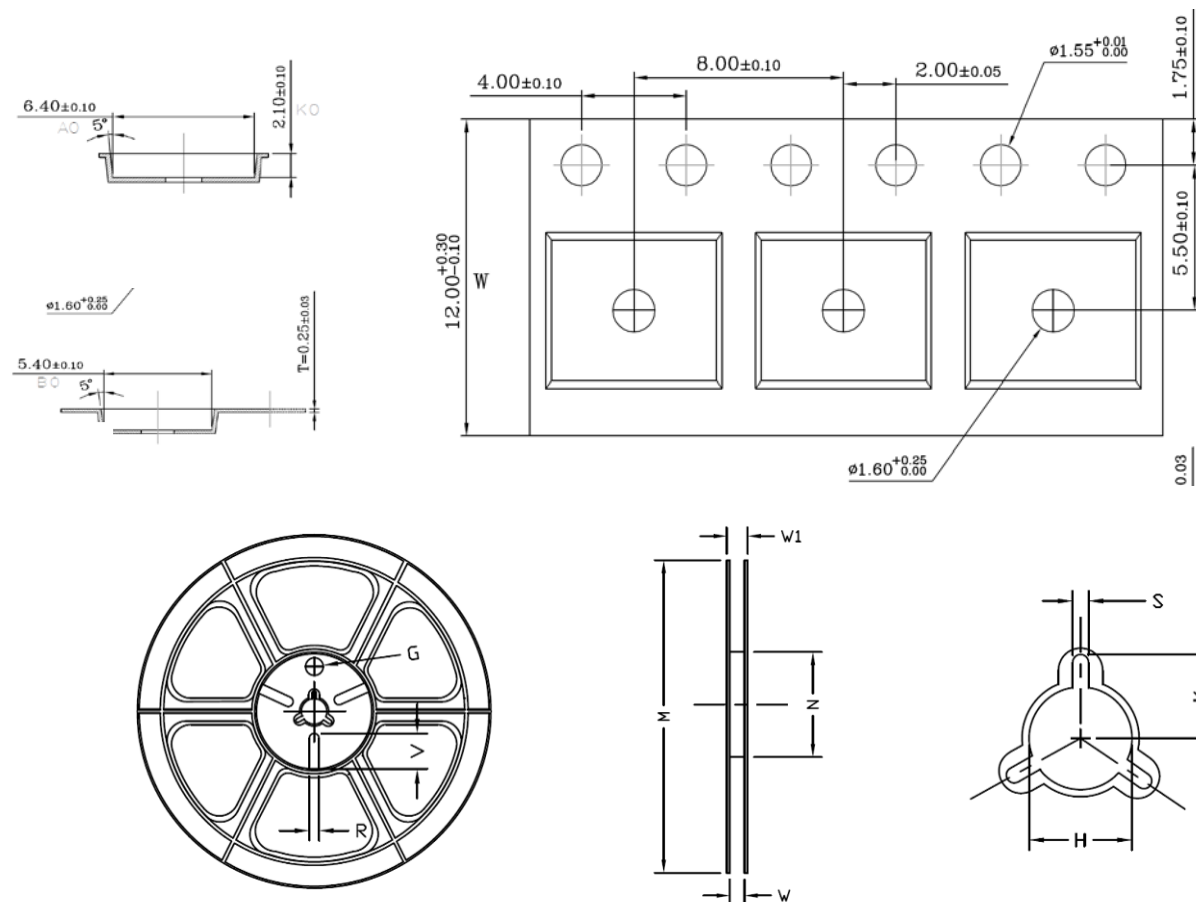
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Recommended Minimum Pads
Dimensions in Inches/(mm)

Tape and Reel

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Tape Size	Reel Size	M	N	W	W1	H	K	S	G	R	V
12mm	Φ330	Φ330.00 ±0.50	Φ97.00 ±0.30	13.00 ±0.30	17.40 ±1.00	Φ13.00 ±0.5	10.6	2.00 ±0.50	—	—	—

Unit Per Reel:
4000pcs

