

## 30V N-Channel Enhancement Mode MOSFET

**Description**

The PECN3118DR uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

**General Features**

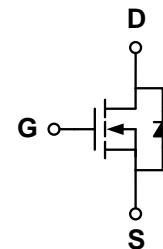
- ◆  $V_{DS} = 30V$ ,  $I_D = 18A$   
 $R_{DS(ON)}(\text{Typ.}) = 10m\Omega$  @  $V_{GS} = 10V$   
 $R_{DS(ON)}(\text{Typ.}) = 13m\Omega$  @  $V_{GS} = 4.5V$
- ◆ High power and current handling capability
- ◆ Lead free product is acquired
- ◆ Surface mount package

**Application**

- ◆ PWM applications
- ◆ Load switch

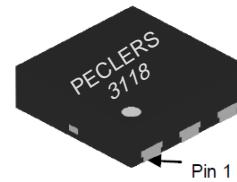
**Package***100% UIS TESTED!*

- ◆ DFN2\*2-6L-B

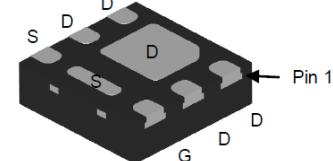
*100%  $\Delta V_{ds}$  TESTED!***Schematic diagram****Marking and pin assignment****DFN2\*2-6L-B**

(Thickness 0.55mm)

Top View



Bottom View

**Ordering Information**

Part Number	Storage Temperature	Package	Devices Per Reel
PECN3118DR	-55°C to +150°C	DFN2*2-6L-B	4000

**Absolute Maximum Ratings (TA=25°C unless otherwise noted)**

parameter	symbol	limit	unit
Drain-source voltage	$V_{DS}$	30	V
Gate-source voltage	$V_{GS}$	$\pm 20$	V
Drain current-continuous	$I_D$	18 <sup>a</sup>	A
		18 <sup>a</sup>	
		18 <sup>a,b,c</sup>	
		13 <sup>b,c</sup>	
Drain-source Diode forward current	$I_S$	18 <sup>a</sup>	A
		2.9 <sup>b,c</sup>	
Maximum power dissipation	$P_D$	18	W

	T <sub>C</sub> =70°C		12	
	T <sub>A</sub> =25°C		3.5 <sup>b,c</sup>	
	T <sub>A</sub> =70°C		2.2 <sup>b,c</sup>	
Operating junction Temperature range		T <sub>j</sub>	-55—150	°C

## Thermal Resistance Ratings

Parameter	Symbol	Typ.	Max.	Unit
Maximum junction-to-ambient <sup>b,d</sup>	R <sub>thJA</sub>	28	36	°C/W
Maximum junction-to-case (drain)	R <sub>thJC</sub>	5.3	6.5	

### Notes:

a. Package limited; b. Surface mounted on 1" x 1" FR4 board; c. t = 5 s;

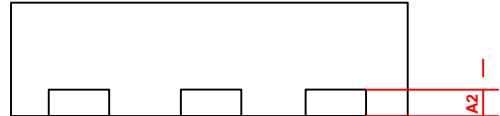
d. Maximum under steady state conditions is 80 °C/W

## Electrical Characteristics (TA=25°C unless otherwise noted)

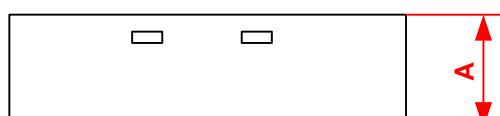
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>OFF Characteristics</b>						
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
<b>ON Characteristics</b>						
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.8	2.5	V
Drain-source on-state resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =18A	-	10	12	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =14A	-	13	16	
Forward transconductance	g <sub>f</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =12A	-	45	-	S
<b>Dynamic Characteristics</b>						
Input capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V f=1.0MHz	-	760	-	pF
Output capacitance	C <sub>OSS</sub>		-	125	-	
Reverse transfer capacitance	C <sub>rss</sub>		-	70	-	
<b>Switching Characteristics</b>						
Turn-on delay time	t <sub>D(ON)</sub>	V <sub>GS</sub> =10V V <sub>DD</sub> =15V R <sub>L</sub> =1.25Ω R <sub>GEN</sub> =3Ω	-	10	20	ns
Rise time	tr		-	11	25	
Turn-off delay time	t <sub>D(OFF)</sub>		-	35	70	
Fall time	tf		-	30	60	
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> =15V I <sub>D</sub> =18A V <sub>GS</sub> =10V	-	14	17	nC
Gate-source charge	Q <sub>gs</sub>		-	2.4	-	
Gate-drain charge	Q <sub>gd</sub>		-	3	-	
<b>DRAIN-SOURCE DIODE CHARACTERISTICS</b>						
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =8A	-	-	1.2	V

## Package Information

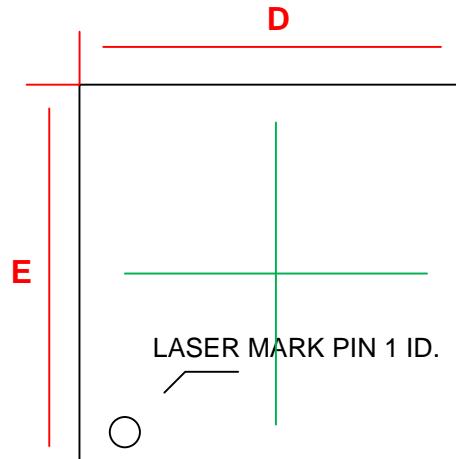
- DFN2\*2-6L-B



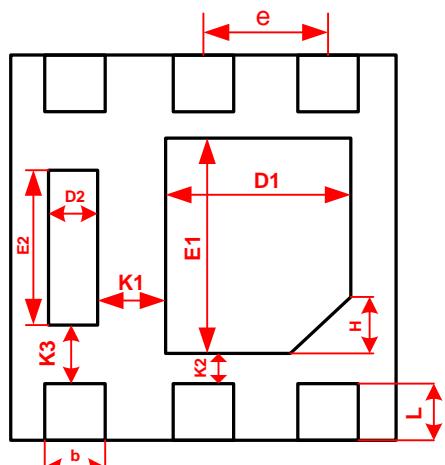
SIDE VIEW



SIDE VIEW



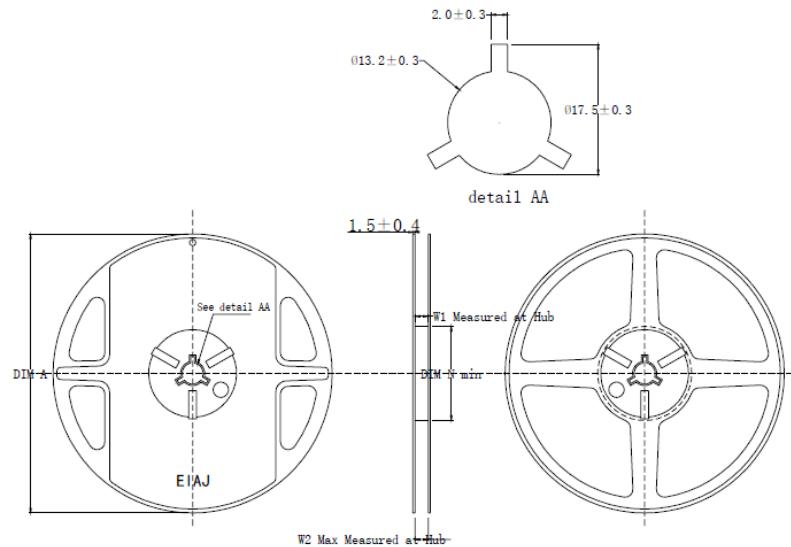
TOP VIEW



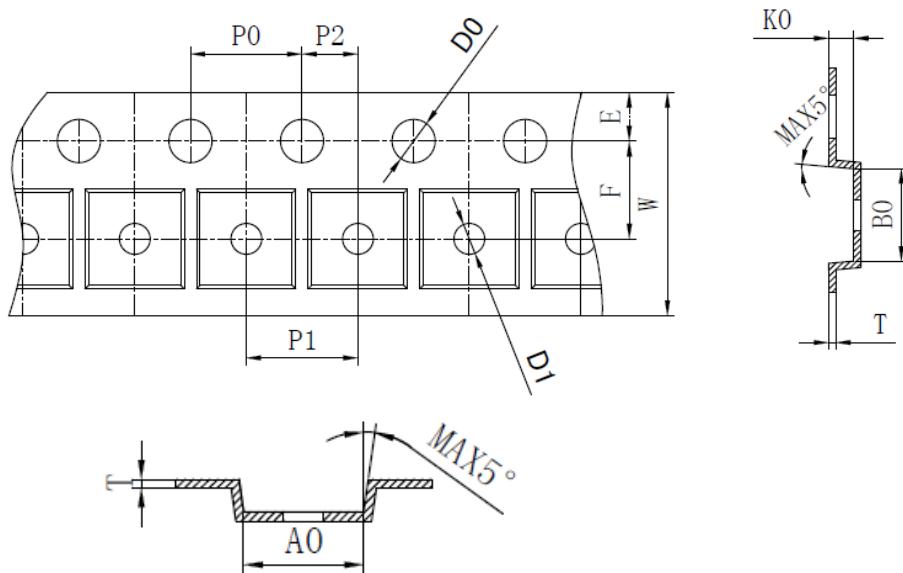
BOTTOM VIEW

PKG	Common Dimension (mm)		
	MIN.	MON.	MAX.
A	0.527	0.552	0.577
A2		0.127REF	
b	0.25	0.30	0.35
D	1.90	2.00	2.10
E	1.90	2.00	2.10
D1	0.85	0.95	1.05
E1	1.05	1.15	1.25
D2	0.20	0.25	0.30
E2	0.69	0.79	0.89
e	0.55	0.65	0.75
H	0.25	0.30	0.35
K1	0.25MIN		
K2	0.15MIN		
K3	0.20MIN		
L	0.20	0.25	0.30

### Tape and Reel



PRODUCT SPECIFICATIONS				
TYPE WIDTH	$\phi A$	$\phi N$	W1 (Min)	W2 (Max)
8MM	$178 \pm 2.0$	$60 \pm 1.0$	8.4	11.4
12MM	$178 \pm 2.0$	$60 \pm 1.0$	12.4	15.4



SYMBOL	A0	B0	K0	P0	P1	P2
SPEC	$2.20 \pm 0.05$	$2.20 \pm 0.05$	$0.75 \pm 0.10$	$4.00 \pm 0.10$	$4.00 \pm 0.10$	$2.00 \pm 0.05$
SYMBOL	T	E	F	D0	D1	W
SPEC	$0.20 \pm 0.03$	$1.75 \pm 0.10$	$3.50 \pm 0.05$	$1.55 \pm 0.05$	$1.00^{+0.10}_{-0}$	$8.00^{+0.20}_{-0.10}$