20V P-Channel Enhancement Mode MOSFET

Description

The PECN2307MR uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 1.8V. This device is suitable for use as a load switch or in PWM applications.

General Features

- V_{DS} =-20V，I_D =-6A
  $R_{DS(ON)}$(Typ.)=24.9 mΩ @V_{GS}=-2.5V
  $R_{DS(ON)}$(Typ.)=19mΩ @V_{GS}=-4.5V
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

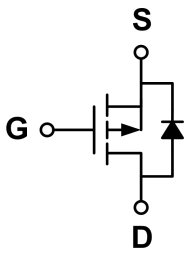
Application

- PWM applications
- Load switch

Package

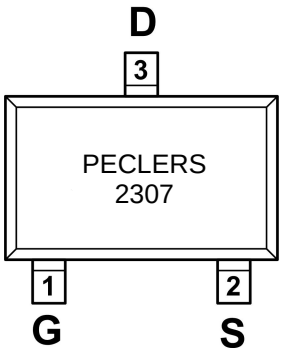
- SOT-23-3L

Schematic diagram



Marking and pin assignment

SOT-23-3L
(TOP VIEW)



Ordering Information

Part Number	Storage Temperature	Package	Devices Per Reel
PECN2307MR	-55°C to +150°C	SOT-23-3L	3000

Absolute Maximum Ratings (TA=25℃ unless otherwise noted)

parameter		symbol	limit	unit
Drain-source voltage		V_{DS}	-20	V
Gate-source voltage		V_{GS}	±12	V
Continuous Drain Current	TC=25°C	I_D	-6	A
	TC=70°C		-4	
Pulsed Drain Current [C]		I_{DP}	-24	A
power dissipation [B]	TC=25°C	P_D	1.4	W
	TC=70°C		0.9	
Junction and Storage Temperature Range		T_J,T_{SGT}	-55—150	℃

Electrical Characteristics (TA=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF Characteristics						
Drain-source breakdown voltage	BV_{DSS}	V_{GS}=0V, I_D=-250μA	-20	-	-	V
Zero gate voltage drain current	I_{DSS}	V_{DS}=-20V, V_{GS}=0V	-	-	-1	μA
Gate-body leakage	I_{GSS}	V_{DS}=0V, V_{GS}=±12V	-	-	±100	nA
ON Characteristics						
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250μA	-0.5	-0.7	-1.0	V
Drain-source on-state resistance	$R_{DS(ON)}$	V_{GS}=-4.5V, I_D=-6A	-	19	25	mΩ
		V_{GS}=-2.5V, I_D=-5A	-	24.9	30	
Forward transconductance	g_{FS}	V_{DS}=-5V, I_D=-6A	-	8	-	S
Dynamic Characteristics						
Input capacitance	C_{ISS}	V_{DS}=-10V ,V_{GS}=0V f=1.0MHz	-	1415	-	pF
Output capacitance	C_{OSS}		-	134	-	
Reverse transfer capacitance	C_{RSS}		-	109	-	
Switching Characteristics						
Turn-on delay time	$t_{D(ON)}$	V_{DD}=-10V I_D=-2.8A V_{GEN}=-4.5V R_L=10ohm R_{GEN}=6ohm	-	25	-	ns
Rise time	tr		-	30	-	
Turn-off delay time	$t_{D(OFF)}$		-	70	-	
Fall time	tf		-	50	-	
Total gate charge	Qg	V_{DS}=-10V,I_D=-6A V_{GS}=-4.5V	-	28.9	-	nC
Gate-source charge	Qgs		-	2.1	-	
Gate-drain charge	Qgd		-	3.6	-	
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode forward voltage	V_{SD}	V_{GS}=0V,Is=-1.25A	-	-0.81	-1.2	V

Thermal Characteristics

Parameter		Symbol	Typ.	Max.	Unit
Maximum Junction-to-Ambient [A]	t≤ 10s	$R_{\theta JA}$	70	90	℃/W
Maximum Junction-to-Ambient [A D]	Steady-State		100	125	
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	62	80	

A. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C. The value in any given application depends on the user's specific board design.

B. The power dissipation PD is based on $T_{J(MAX)}$=150°C, using ≤ 10s junction-to-ambient thermal resistance.

C. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$=150°C. Ratings are based on low frequency and duty

Typical Performance Characteristics

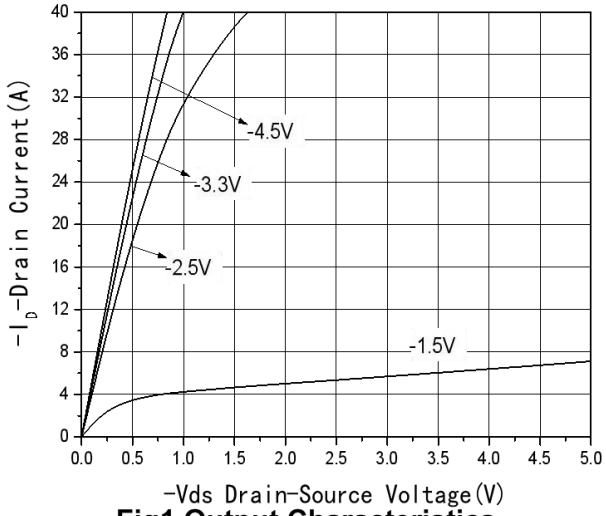


Fig1 Output Characteristics

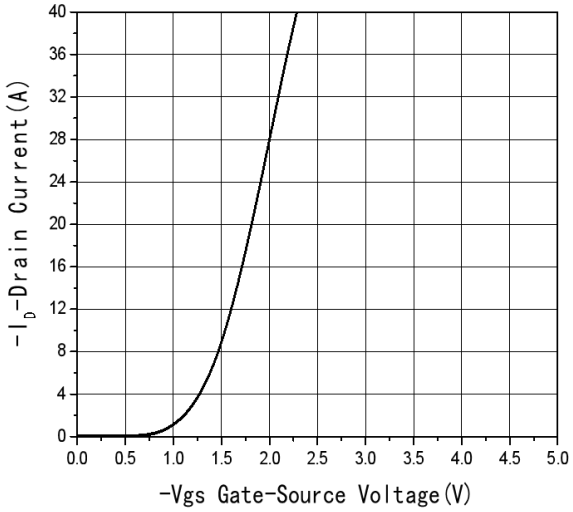


Fig2 Transfer Characteristics

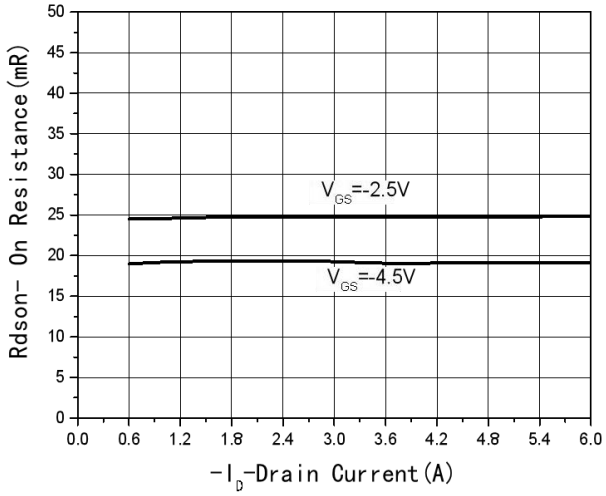


Fig3 Rdson-Drain current

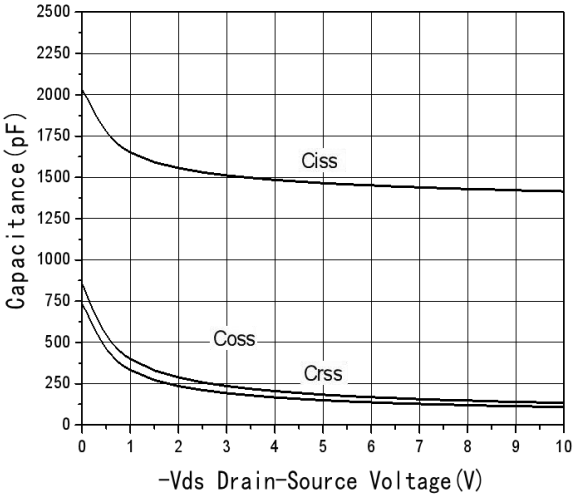


Fig4 Capacitance vs Vds

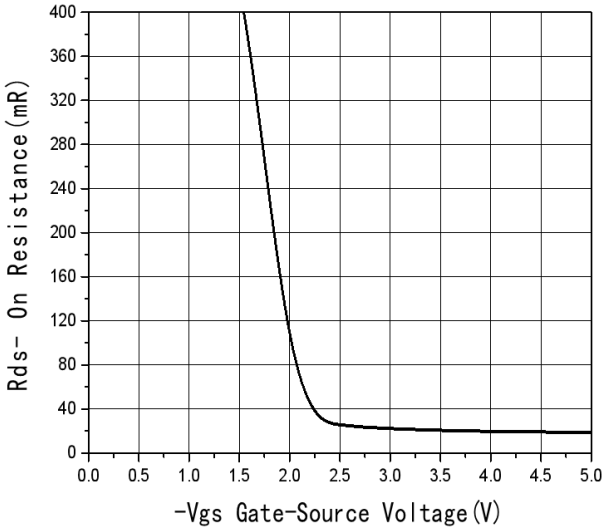


Fig5 Rdson-Gate Drain voltage

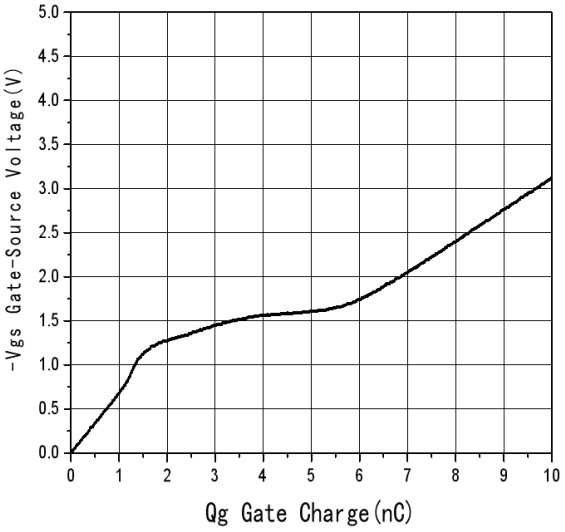


Fig6 Gate Charge

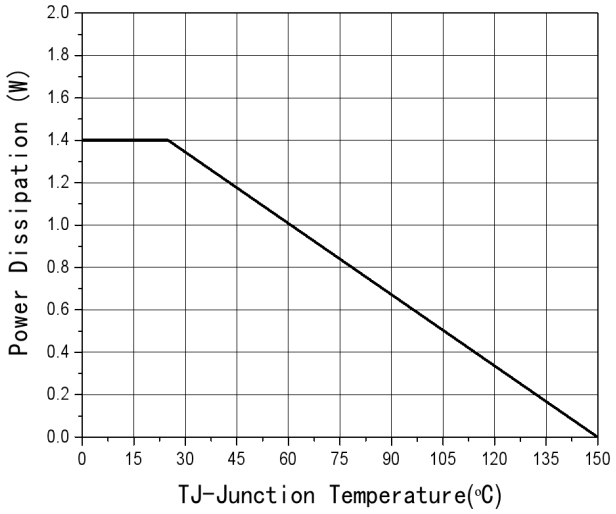


Fig7 Power De-rating

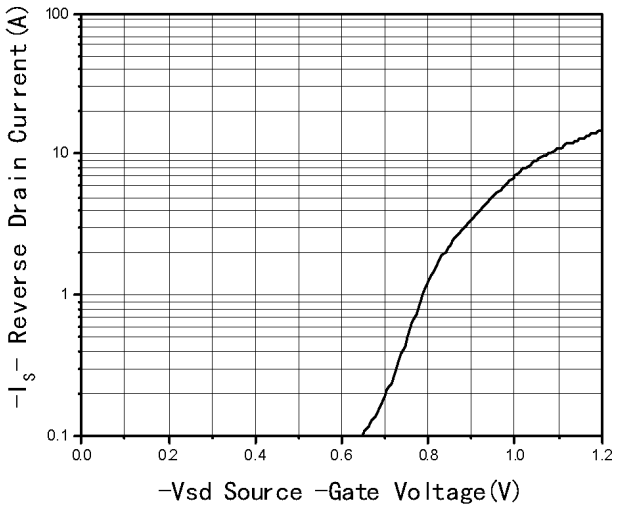
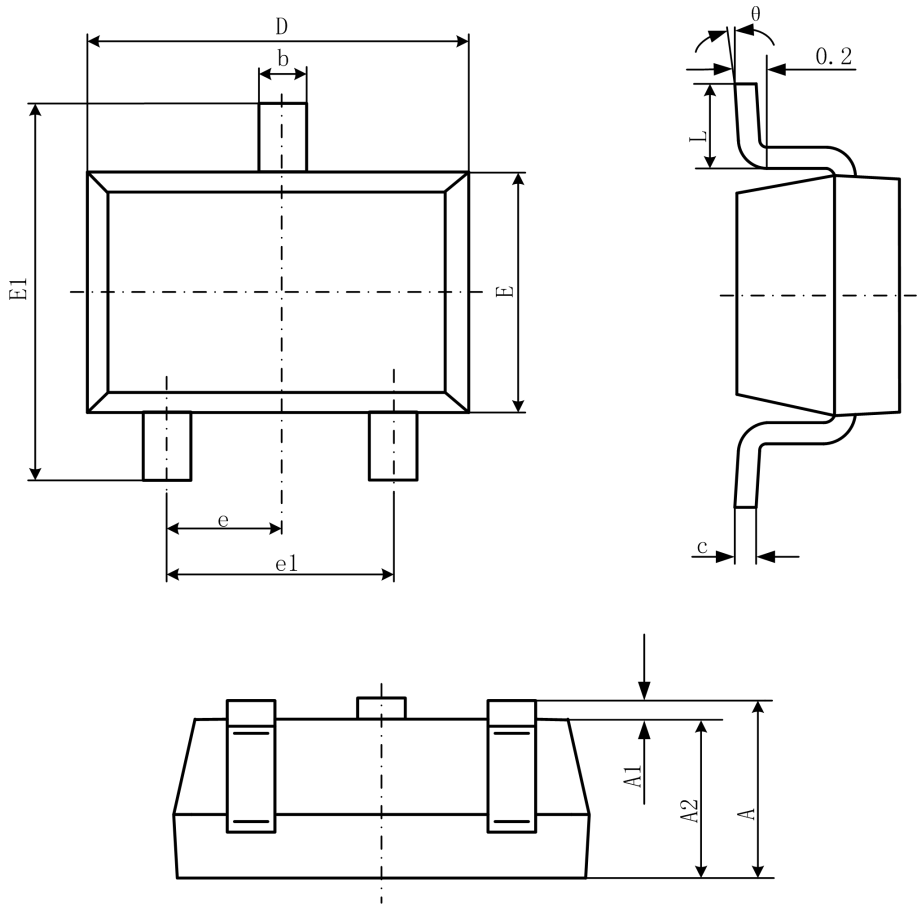


Fig8 Source-Drain Diode Forward

Package Information

- **SOT-23-3L**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°