

REVERSE VOLTAGE: 50 to 1000 VOLTS

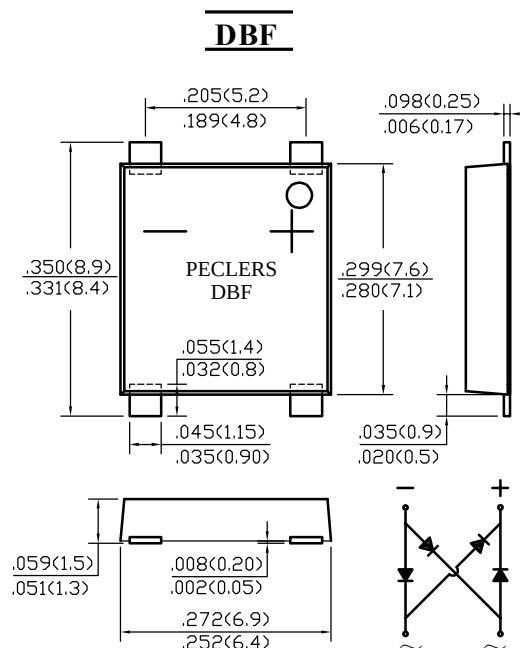
FORWARD CURRENT: 6.0 AMPERE

FEATURE

- . Glass passivated junction.
- . Ideal for printed circuit board.
- . Reliable low cost construction utilizing molded plastic technique.
- . High surge current capability.
- . High temperature soldering guaranteed: 260°C/10 seconds at terminals.

MECHANICAL DATA

- . Case: Molded plastic, DBF
- . Epoxy: UL 94V-O rate flame retardant
- . Terminals: Leads solderable per MIL-STD-202, method 208 guaranteed
- . Mounting position: Any
- . Weight: 0.204g (approximately)



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	SYM BOL	DBF 601	DBF 602	DBF 603	DBF 604	DBF 605	DBF 606	DBF 607	units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward rectified Current @ T _A =40°C	$I_{F(AV)}$	6.0							A
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I_{FSM}	100							A
Maximum Instantaneous Forward Voltage @I _F =3.0A DC @I _F = 6.0A DC	V_F	1.0 1.1							V
Maximum DC Reverse Current @T _J =25°C at rated DC blocking voltage @T _J =125°C	I_R	5.0 500.0							μA
I ² t Rating for Fusing (t < 8.3ms)	I^2t	41.5							A ² Sec
Typical Junction Capacitance Per Leg (Note1)	C_J	35							pF
Typical Thermal Resistance (Note2)	R_{JA}	15							°C /W
	R_{JC}	7							
Storage Temperature	T_{STG}	-55 to +150							°C
Operating Junction Temperature	T_J	-55 to +150							°C

Note:

1. Measured at 1.0 MHz and applied reverse voltage of 4.0Vdc
2. Thermal resistance from Junction to case, lead and ambient in accordance with JESD-51.
Unit mounted on 15mm*12mm*1.6mm AL pad attach 195mm*195mm*10mm steel plate

RATINGS AND CHARACTERISTIC CURVES

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

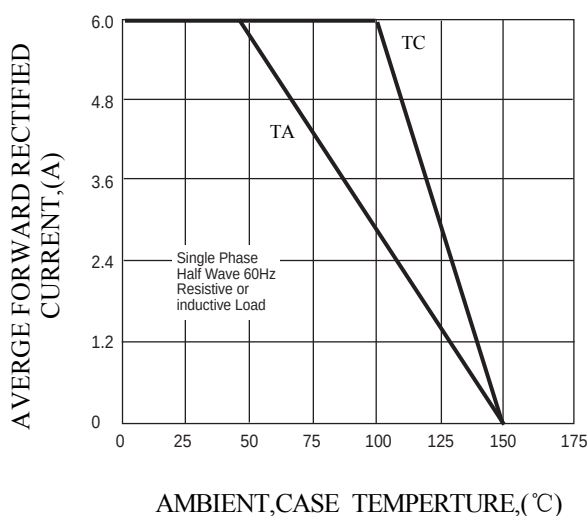


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

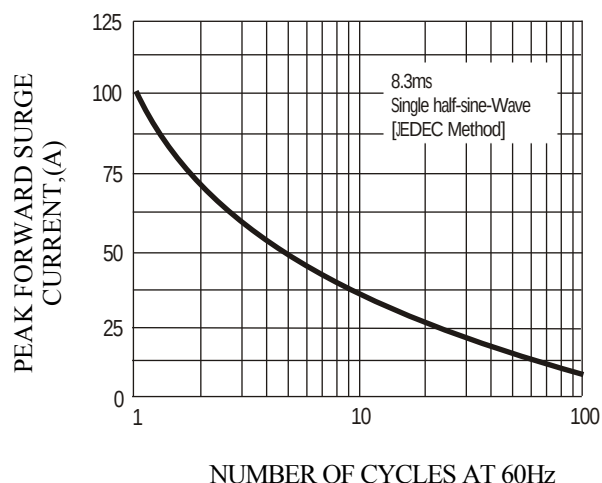


FIG.5-TYPICAL JUNCTION CAPACITANCE

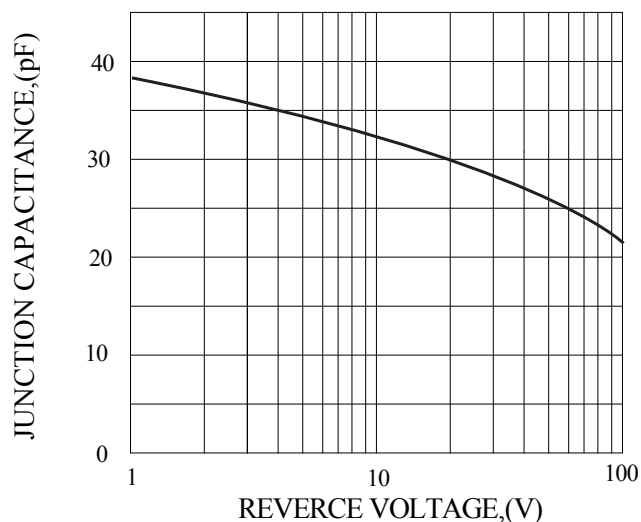


FIG.2-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

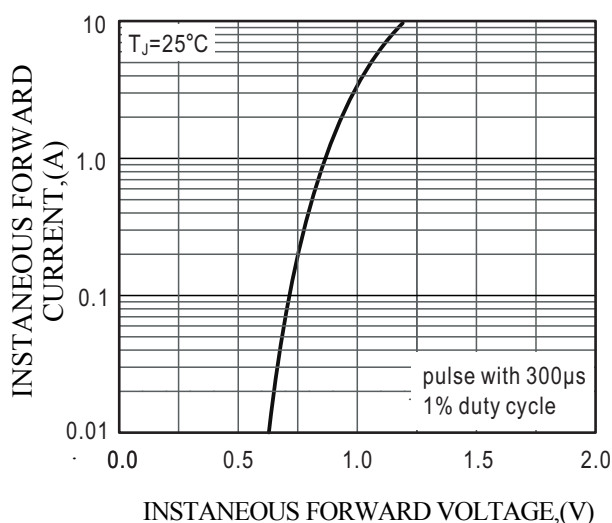


FIG.4-TYPICAL REVERSE CHARACTERISTICS

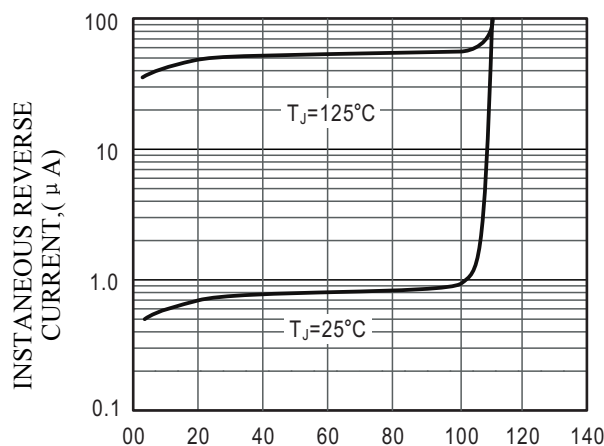


FIG.6 MOUNTING PAD LAYOUT

