

**Reverse Voltage - 50 to 1000 V**

**Forward Current - 1 A**

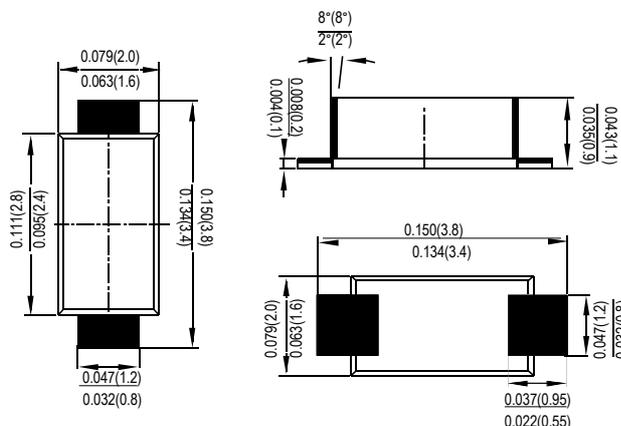
### FEATURES

- For surface mounted applications
- Low profile package
- Glass Passivated Chip Junction
- Ideal for automated placement
- Fast reverse recovery time
- Lead free in comply with EU RoHS 2011/65/EU directives

### MECHANICAL DATA

- Case: SOD-123FL
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 15mg 0.00048oz

### SOD-123FL



Dimensions in inches and (millimeters)

### Maximum Ratings and Electrical characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

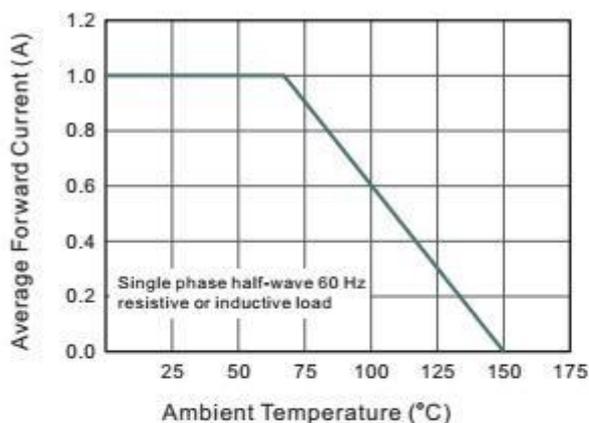
Parameter	Symbols	A1	A2	A3	A4	A5	A6	A7	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current at $T_a = 65\text{ °C}$	$I_{F(AV)}$	1							A
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)	$I_{FSM}$	30							A
Maximum Instantaneous Forward Voltage at 1 A	$V_F$	1.1							V
Maximum DC Reverse Current at Rated DC Blocking Voltage $T_a = 25\text{ °C}$ $T_a = 125\text{ °C}$	$I_R$	5 100							$\mu\text{A}$
Typical Junction Capacitance (Note1)	$C_j$	4							pF
Typical Thermal Resistance (Note2)	$R_{\theta JA}$	60							$^{\circ}\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_j, T_{stg}$	-55 ~ +150							$^{\circ}\text{C}$

### Notes:

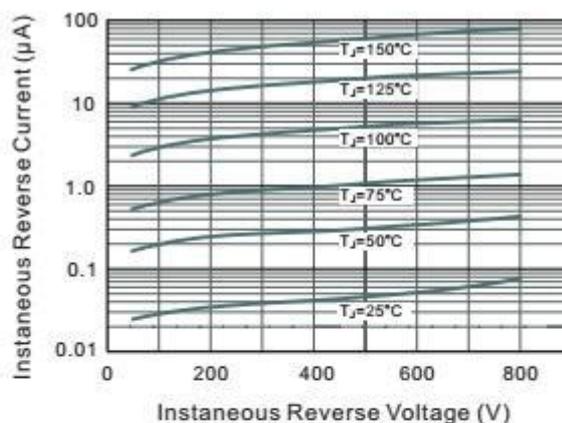
- 1- Measured at 1 MHz and applied reverse voltage of 4 V D.C
- 2- P.C.B. mounted with 0.2x0.2" (5.0x5.0mm) copper pad areas.

### Characteristic Curves ( $T_A=25\text{ }^\circ\text{C}$ unless otherwise noted)

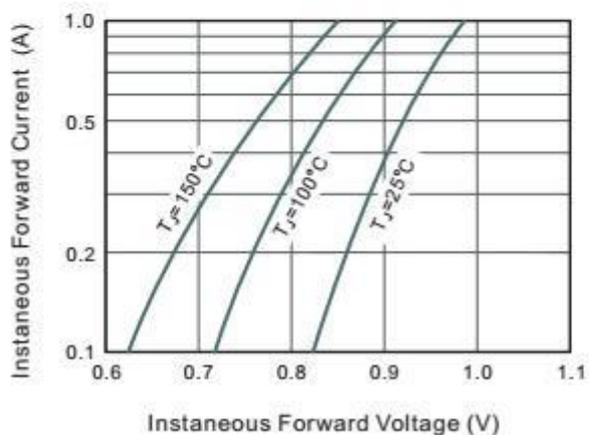
**Fig.1 Forward Current Derating Curve**



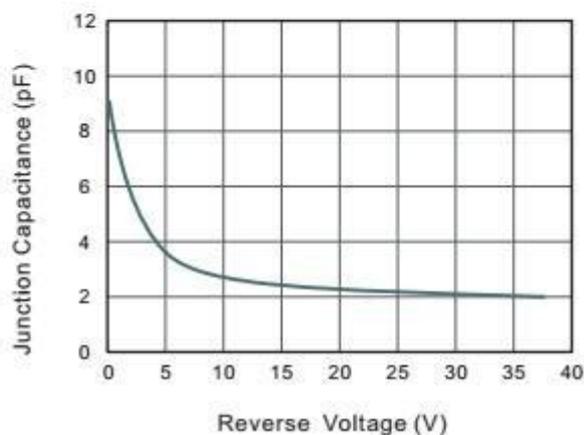
**Fig.2 Typical Instantaneous Reverse Characteristics**



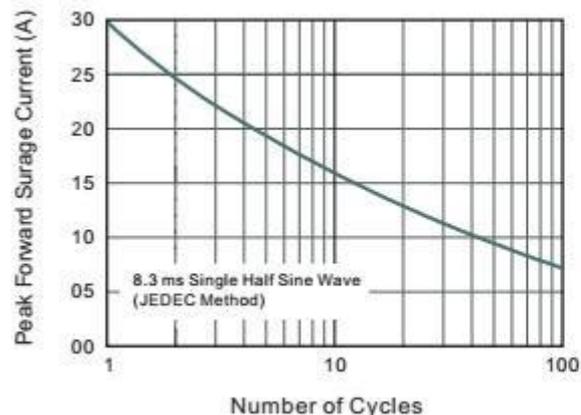
**Fig.3 Typical Forward Characteristic**



**Fig.4 Typical Junction Capacitance**



**Fig.5 Maximum Non-Repetitive Peak Forward Surge Current**



**Fig.6 TYPICAL CAPACITANCE**

